

Electronic Acknowledgement Receipt

EFS ID:	2097686
Application Number:	10791268
International Application Number:	
Confirmation Number:	8944
Title of Invention:	DATA MODULATION METHOD AND APPARATUS CAPABLE OF SUPPRESSING DC COMPONENT USING PARITY INFORMATION OF SYNCHRONIZATION CODEWORD
First Named Inventor/Applicant Name:	Jae-seong Shim
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Filer Authorized By:	Michael D. Stein
Attorney Docket Number:	1793.1211
Receipt Date:	17-AUG-2007
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Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	yes
Payment was successfully received in RAM	\$ 1700
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Deposit Account	

File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
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1		Fees_Comments_8-17-07.pdf	366821 b1ce237de1d2bc2281eab42ed224b0dd7aca85a23	yes	3
Multipart Description/PDF files in .zip description					
Document Description			Start	End	
Issue Fee Payment (PTO-85B)			1	1	
Post Allowance Communication - Incoming			2	3	
Warnings:					
Information:					
2	Fee Worksheet (PTO-06)	fee-info.pdf	8369 ee218bdc38e1eab0a236d8317a1507e73ac7db7b	no	2
Warnings:					
Information:					
Total Files Size (in bytes):			375190		
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